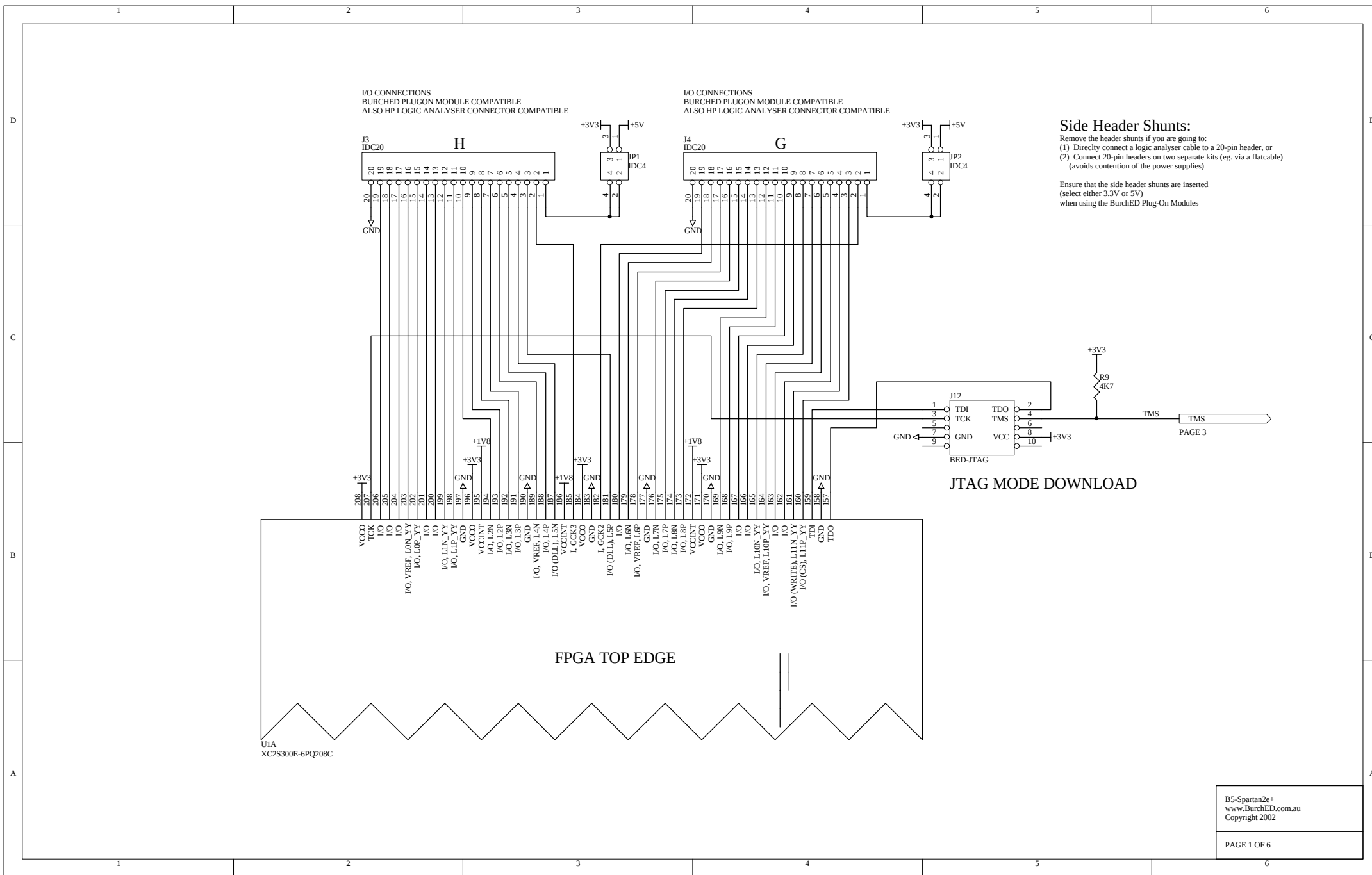
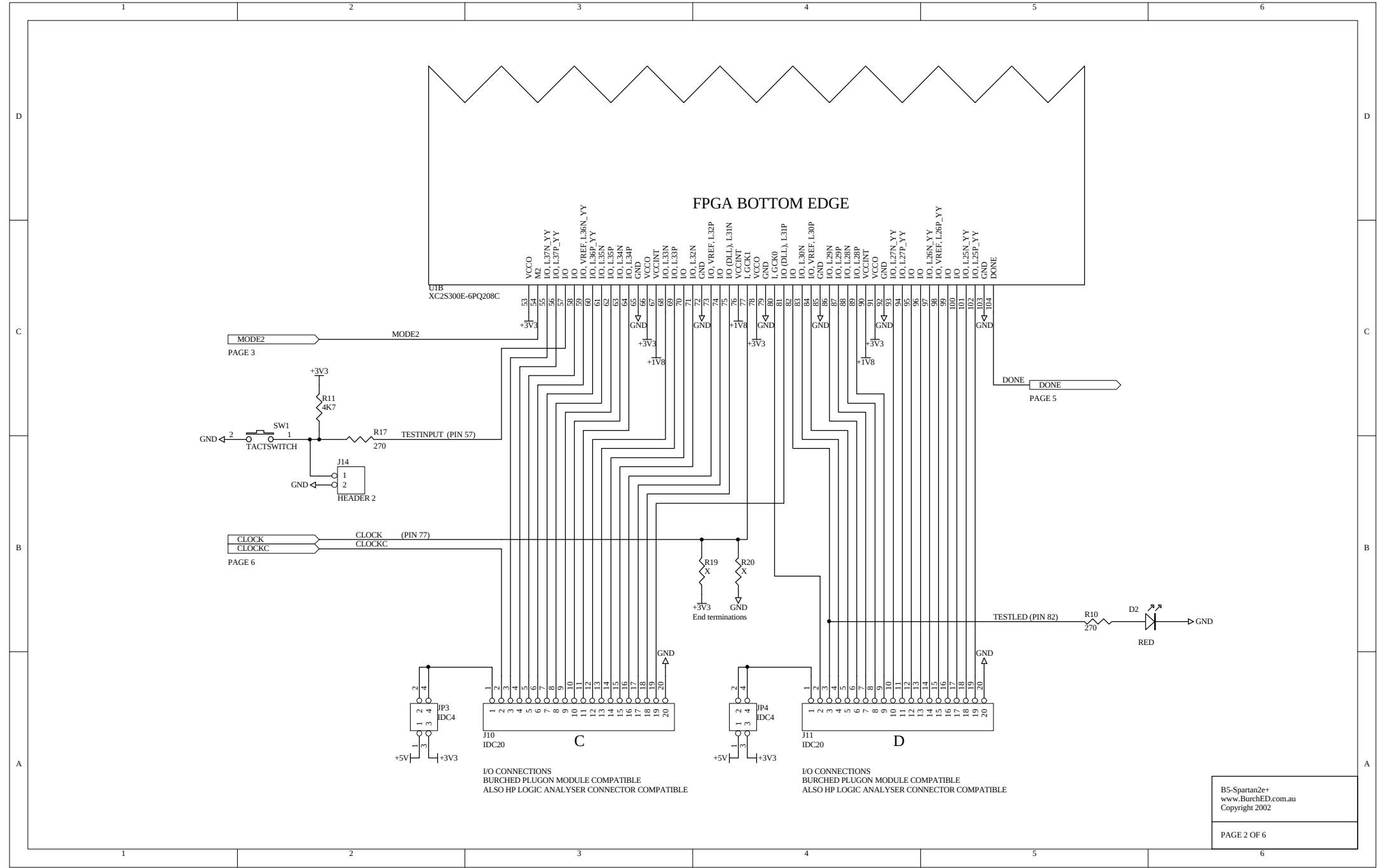
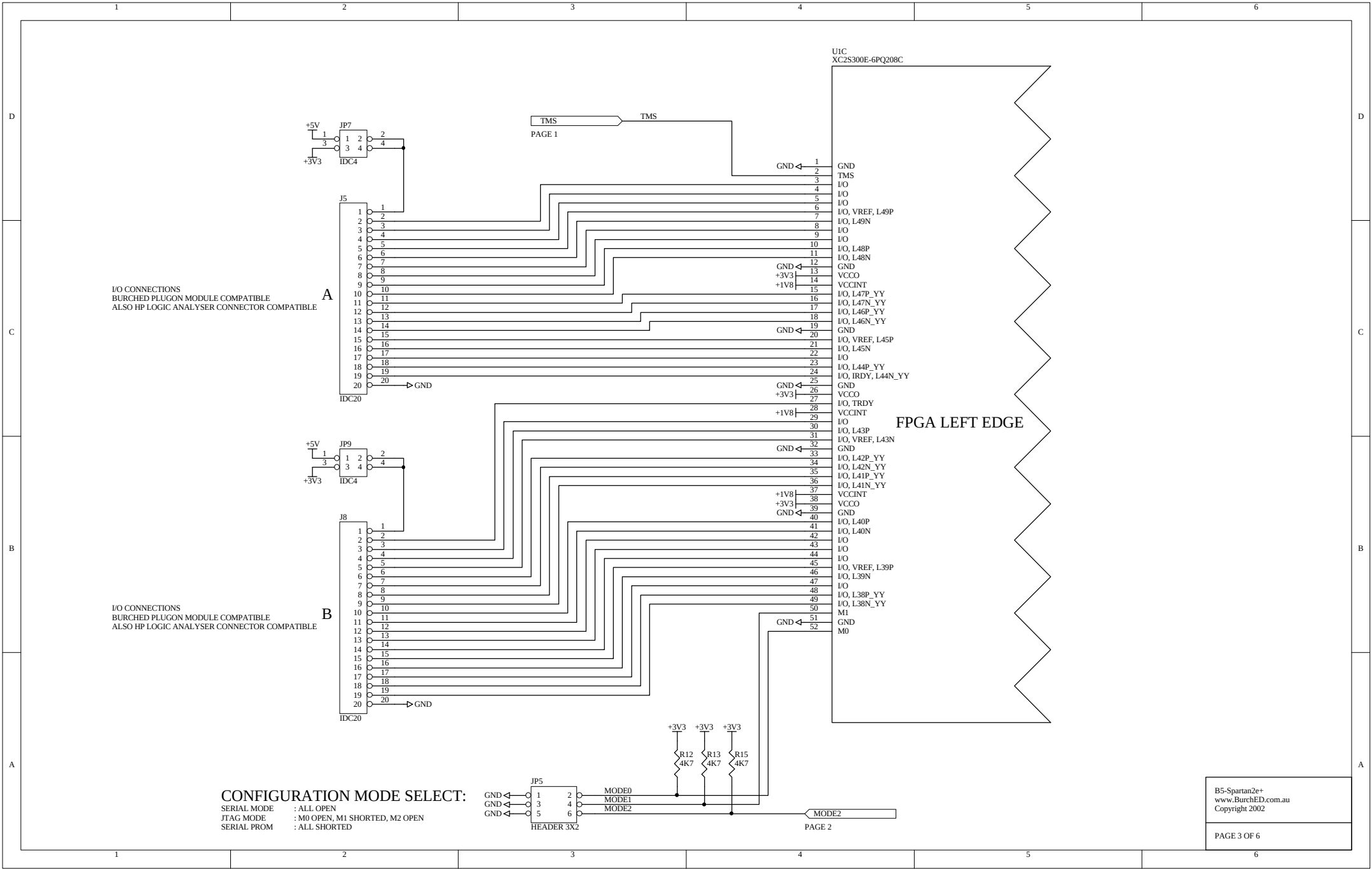






I/O CONNECTIONS
BURCHED PLUGON MODULE COMPATIBLE
ALSO HP LOGIC ANALYSER CONNECTOR COMPATIBLE

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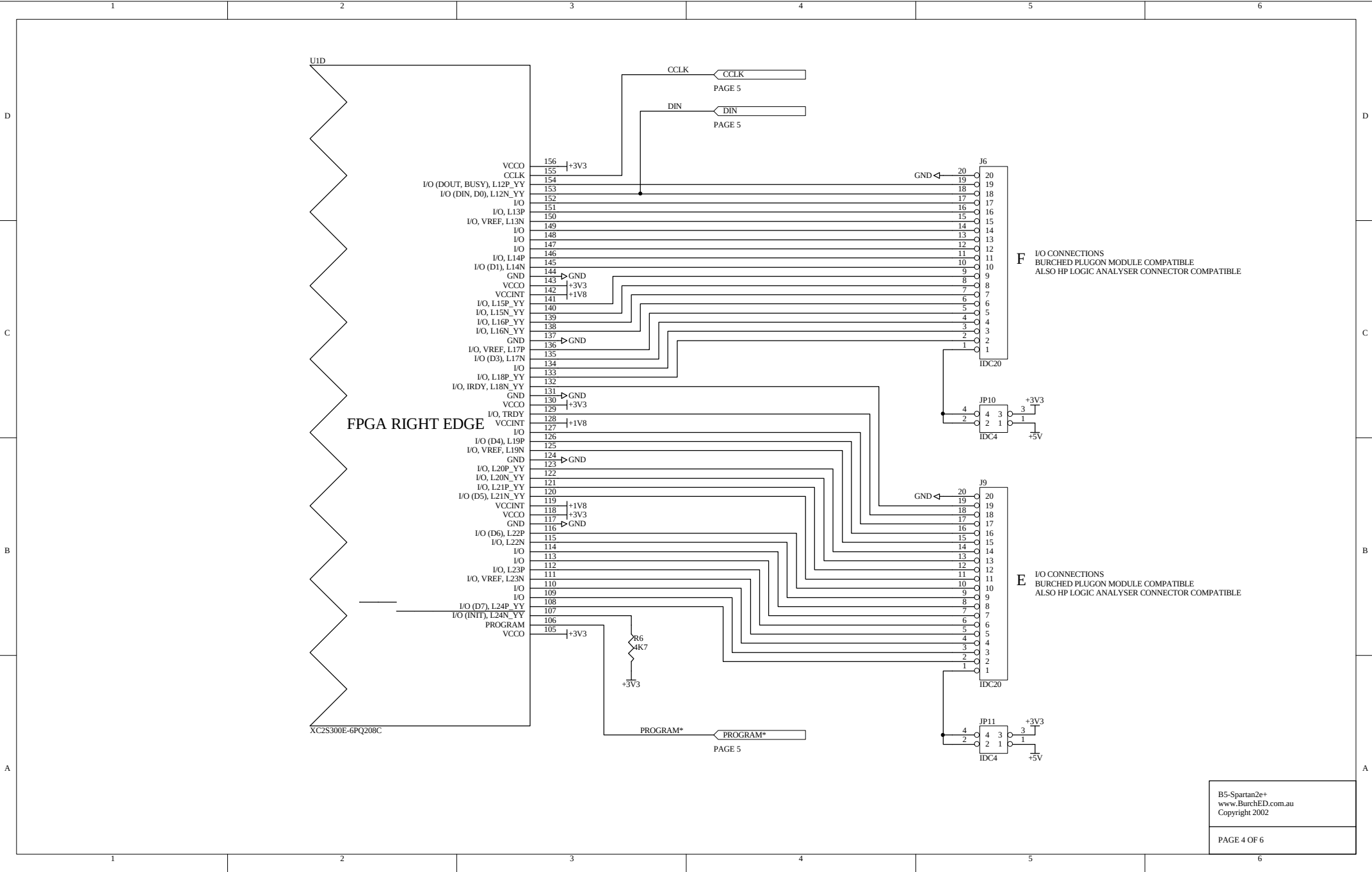
I/O CONNECTIONS
BURCHED PLUGON MODULE COMPATIBLE
ALSO HP LOGIC ANALYSER CONNECTOR COMPATIBLE

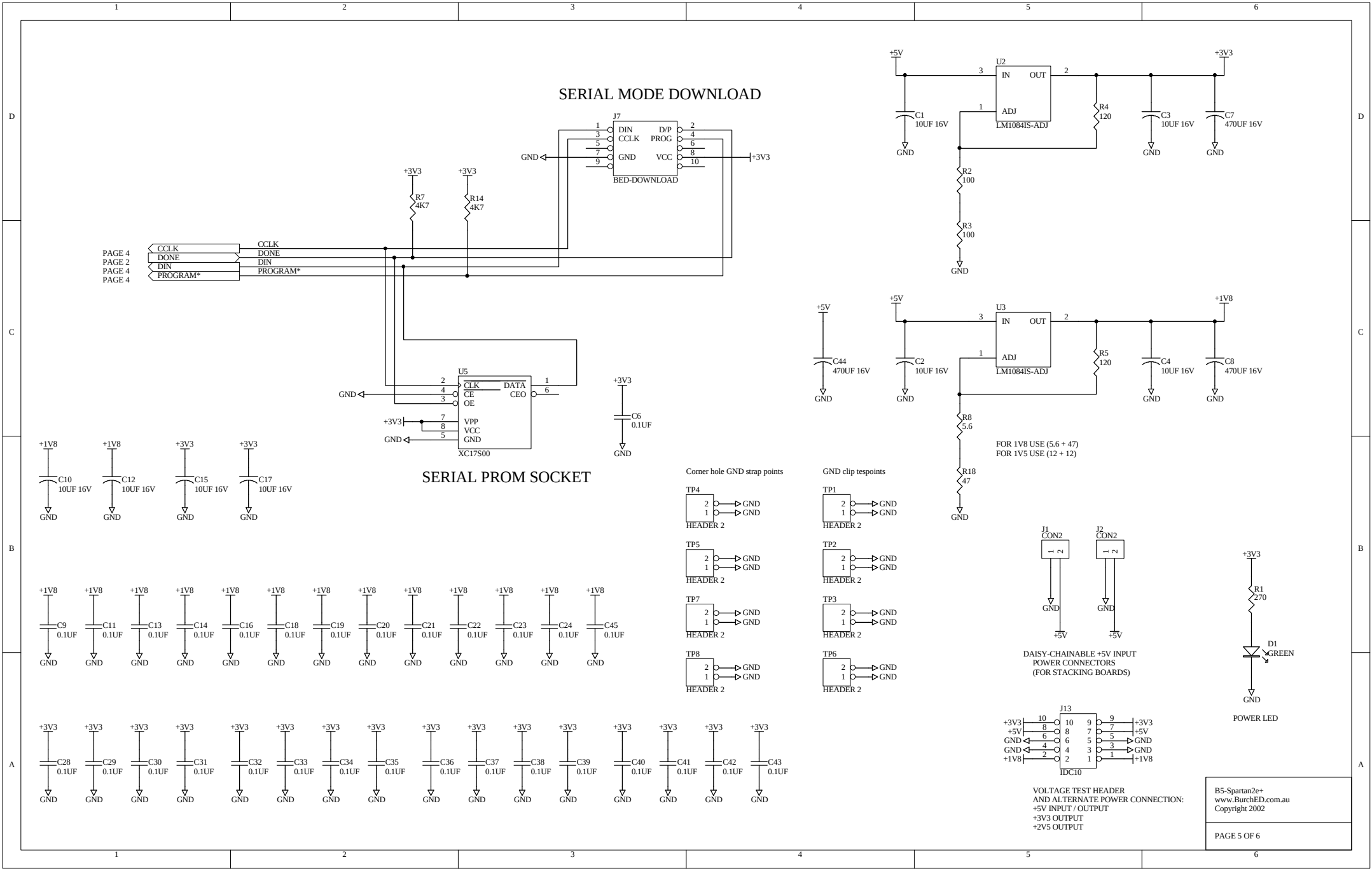
CONFIGURATION MODE SELECT:

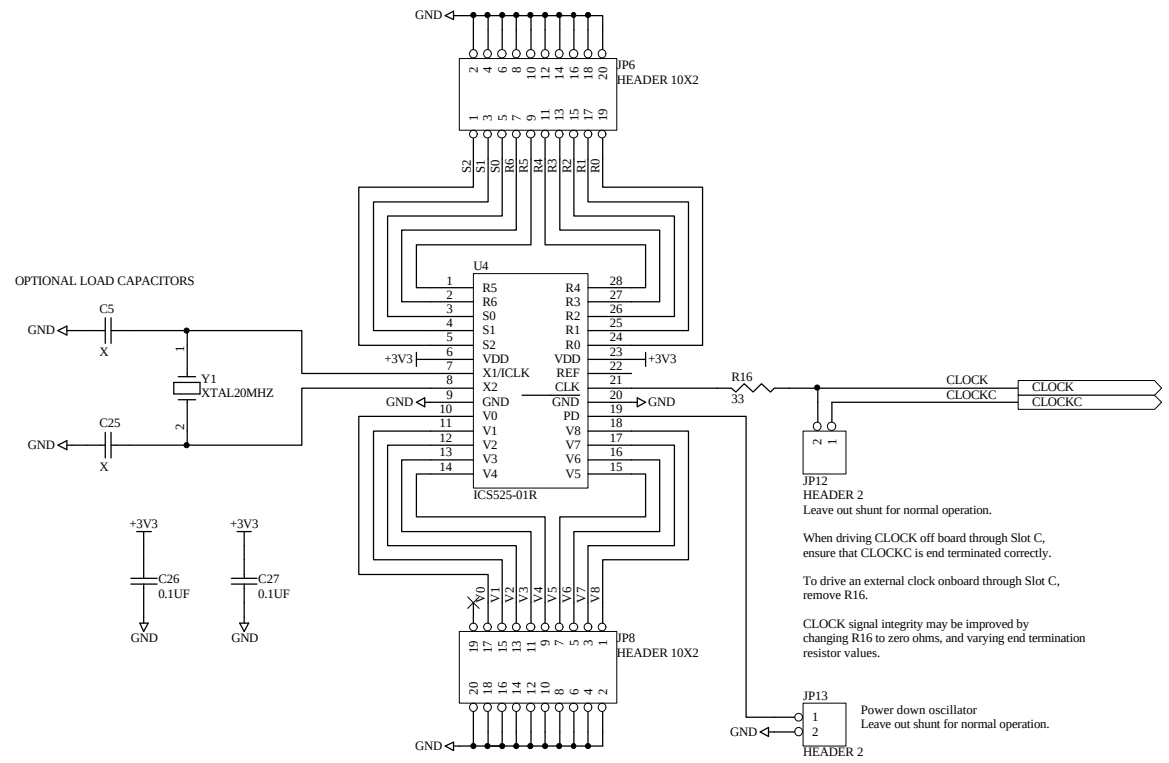
SERIAL MODE : ALL OPEN
JTAG MODE : M0 OPEN, M1 SHORTED, M2 OPEN
SERIAL PROM : ALL SHORTED



FPGA LEFT EDGE







PROGRAMMABLE PLL OSCILLATOR GENERATES ANY FREQUENCY BETWEEN 1MHZ AND 100MHZ

USE THE ONLINE ICS525-01 CALCULATOR AT:
www.icst.com/products/ics525inputForm.html
 (for the calculator use an input of 20MHz and supply of 3.3V)

EXAMPLE PLL OSCILLATOR SETTINGS:
 (0 = SHORTED, 1 = OPEN)

	1MHz	3.6864MHz (-34 ppm)	4MHz	20MHz	24MHz	25.175 MHz (+17.4 ppm)	48MHz	66MHz	80MHz	100MHz
S2	0	0	0	0	1	1	0	0	0	0
S1	0	0	0	1	0	0	0	0	0	0
S0	0	0	0	0	0	1	1	1	1	1
R6	0	0	0	0	0	1	0	0	0	0
R5	1	1	0	0	0	0	0	0	0	0
R4	0	1	0	0	0	0	0	0	0	0
R3	1	0	1	0	0	1	0	1	0	0
R2	1	0	0	0	0	0	0	0	0	0
R1	1	0	1	0	1	1	1	0	0	0
R0	0	1	0	1	0	0	1	0	1	1
V8	0	0	0	0	0	1	0	0	0	0
V7	0	0	0	0	0	0	0	0	0	0
V6	0	0	0	0	0	0	0	0	0	0
V5	0	1	0	0	0	0	0	0	0	0
V4	0	0	0	0	0	1	0	1	0	0
V3	0	0	0	0	0	0	0	0	0	0
V2	1	1	1	1	1	1	1	0	1	1
V1	0	1	0	0	0	1	0	0	0	1
V0	0	1	0	0	0	1	0	1	0	1